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Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (previously presented) A voltage driver circuit for driving a device at a selected one of a plurality of voltages associated with respective device operations including a high voltage operation and a relatively lower voltage operation, the driver circuit comprising an input (IN), a single output (OUT) for connection to said device, and a plurality of voltage drivers between said input and said output including at least one high voltage breakdown driver and at least one relatively lower breakdown voltage driver the circuit being arranged such that, during a high voltage operation, said high voltage breakdown driver is connected to said output and there is a substantially zero voltage drop across said relatively lower breakdown voltage driver, and, during a relatively lower voltage operation, said relatively lower breakdown voltage driver provides the drive voltage for driving said device, the contribution of said high breakdown voltage driver to said drive voltage during said relatively lower voltage operation being substantially negligible.

2. (previously presented) A circuit according to claim 1, wherein the high voltage breakdown drivers comprise inverters consisting of high voltage breakdown transistors.

3. (previously presented) A circuit according to claim 1, wherein the at least one relatively lower breakdown voltage driver comprises an inverter consisting of relatively lower breakdown voltage transistors.

4. (previously presented) A circuit according to claim 1, comprising two signal paths between the input and the output, a first signal path consisting of one or more high voltage drivers connected in series, and a second signal path consisting of at least one low

voltage driver, the first and second signal paths being connected in parallel to one another.

5. (original) A circuit according to claim 4, comprising means for selecting the first signal path during high voltage operation.

6. (currently amended) A voltage driver circuit for driving a device at a selected one of a plurality of voltages associated with respective device operations including a high voltage operation and a relatively lower voltage operation, the driver circuit comprising an input (IN), a single output (OUT) for connection to said device, and a plurality of voltage drivers between said input and said output including at least one high voltage breakdown driver and at least one relatively lower breakdown voltage driver the high breakdown voltage driver comprising a voltage level shifter which is connected at the input of the circuit between first and second voltage lines, the output of said level shifter being connected to the input of a relatively lower breakdown voltage driver connected to the output between said first and second voltage lines, the voltage driver circuit being arranged such that, during a high voltage operation, there is a substantially zero voltage drop across said relatively lower breakdown voltage driver.

7. (original) A circuit according to claim 6, wherein said voltage level shifter comprises a partial level shifter.

8. (previously presented) A circuit according to claim 6 wherein the relatively lower breakdown voltage driver comprises an inverter consisting of thick gate oxide devices.

9. (previously presented) A circuit according to claim 8, wherein the thick gate oxide devices comprise GO_2 devices.

10. (previously presented) A circuit according to claim 6, wherein said at least one relatively lower breakdown voltage driver comprises an I/O protection inverter.

11. (previously presented) A circuit according to claim 6, wherein a high voltage pull-up or pull-down transistor is provided between the output and the first or second voltage lines respectively.

12. (previously presented) A memory device, comprising a voltage driver circuit according to claim 1.

13. (original) An integrated circuit, comprising or including a memory device according to claim 12.

14. (original) A computing system, including an integrated circuit according to claim 13.